

# ABSTRACT OF THE DISCLOSURE

A ramp waveform generation circuit for generating a ramp waveform to be applied to a capacitive load serving as a display element is connected between the ground and a signal line for supplying a high-level voltage generated by a power supply circuit for generating a voltage to be applied to the capacitive load, thereby operating the ramp waveform generation circuit with reference to the ground potential. Hence, without using a plurality of power supply circuits or a signal transmission circuit for converting the reference potential of a control signal for the ramp waveform generation circuit, a stable ramp waveform can be output with a simple circuit arrangement.

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